

PERFORMANCE, POWER, AND ENERGY OF IN-SITU AND POST-PROCESSING VISUALIZATION: A CASE STUDY IN CLIMATE SIMULATION

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Introduction

- Off-chip data movement can consume hundreds of times as much energy as on-chip data movement
- More data produced from high-resolution simulation to increase fidelity → More power/energy for storage subsystem
- Problematic because future supercomputers will be power-limited

Operation	Energy (pJ)
DF FLOP	10
Register	1
1mm on-chip	3-5
5mm on-chip	20
Off-chip	1000-2000

Energy consumption projection for an exascale system [1]

Hypothesis

Reducing disk reads and writes using the following techniques will save significant amount of energy and power:

- Temporal sampling – Write output only every few time steps
- In-situ visualization – Produce images *during* simulation (without writing raw data to the disk) and write only the compact image representation

Experimental Setup

Single-Node Setup

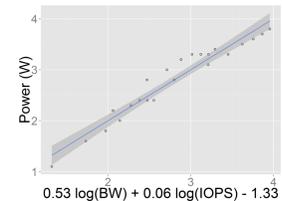
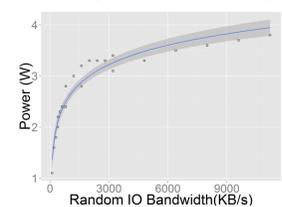
Component	Detail
Processor	2x Intel Xeon E5-2665 @ 2.4GHz
DRAM	4x 16GB DR3-1333
Disk	500GB Seagate 7200rpm

Power Measurement

Power measured at 1-Hz frequency using the following methods for different components:

- Full system** – WattsUp Pro power meter
- Processor and DRAM** – Intel RAPL interface (statistical model based on performance counters)
- Disk** – Statistical power model based on *iostat* statistics

Disk power modeling

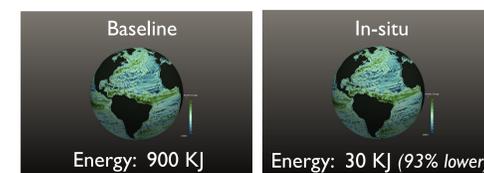


$$\text{Disk Power} = 5.67 + 0.53 \cdot \log(\text{BW}) + 0.06 \cdot \log(\text{IOPS})$$

HPC System Setup

- Compute cluster
 - 128 nodes of Caddy supercomputer
 - 2x Intel E5-2670 CPU/node
 - 64 GB RAM/node
 - Power measured for 10 nodes using cage power meter and extrapolated
- Storage cluster
 - 5 nodes running Lustre file system
 - 1 master node, 2 metadata servers, 2 object storage servers
 - Intelligent PDUs for power measurement

Application



Same cognitive value for both visualization pipelines

MPAS Ocean simulation

Ocean component of the modeling for prediction across scale (MPAS-O) [2] solves an unstructured mesh problem to calculate the Okuba-Weiss metric. The end goal is to identify eddies in the ocean (shown in figure). Visualization through Paraview-Cinema [4].

Problem Size: 240-km grid run for simulated period of one month

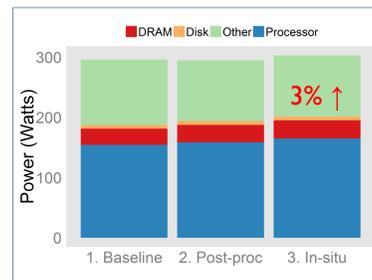
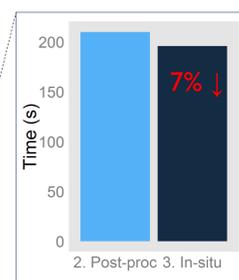
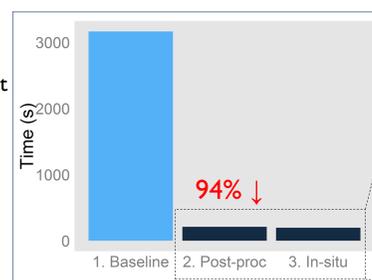
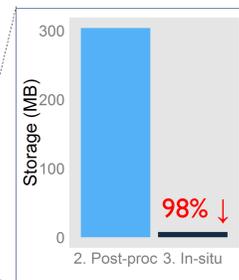
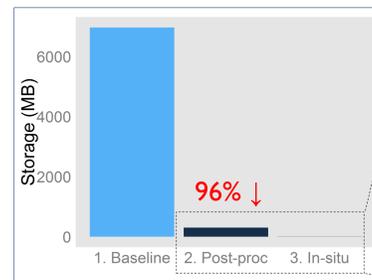
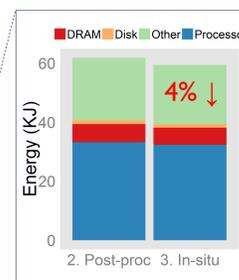
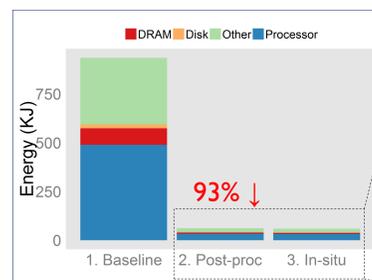
Results

Visualization Pipelines Evaluated

- Baseline – “Traditional” post-processing *without* any sampling
- Post-processing – “Modern” post-processing with *temporal sampling* (i.e., write every *n* iterations – in this case, *n* = 24)
- In-situ – Produce images *in situ* alongside simulation and write compact image representation once every 24 iterations)

Key Findings

- In-situ Visualization vs. Baseline (“Traditional” Post-Process)
 - Saves 93% energy for MPAS-O for the given problem size ... despite consuming 3% more power on average ... but amortized by 94% faster execution from reduced I/O wait
- In-situ Visualization vs. Post-processing (“Modern” Post-Process)
 - Saves 4% energy for MPAS-O for the given problem size ... despite consuming 3% more power on average ... but amortized by 7% faster execution from reduced I/O wait
- Energy saved from disk subsystem almost negligible
 - Nearly all energy saved from reduced system idling
- 97.5% lower storage requirement for in-situ pipeline



Conclusion

In-situ visualization offers the following advantages:

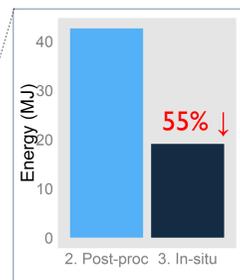
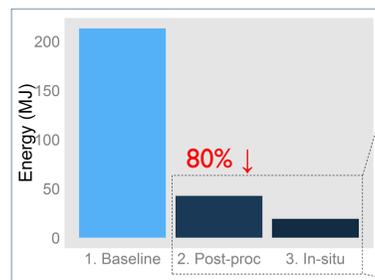
- Reduced energy consumption (by reducing system idling or I/O wait time)
- Reduced power (by using fewer storage nodes)
- Improved performance (by reducing I/O wait time and by making more power available for compute nodes)

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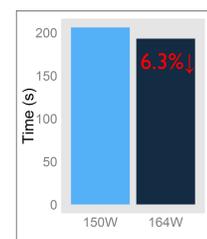
Preliminary Results at Scale

- Problem size: 60-km grid size
- Sampling rate: One output per simulated day
- Key finding: 55% energy savings for in-situ pipeline (vs. modern post-processing pipeline)
- More aggressive sampling possible to save more energy, but risks missing important events of simulation



Implications

- Lower storage requirements → **Fewer I/O nodes**
- Fewer I/O nodes → **More power for compute nodes**
 - Assuming 10% nodes reserved in a HPC data center for storage,
 - **data center power goes down by ~ 10%**
 - Estimated increase in power budget for compute nodes ~ 10%
 - **6.3% improvement in performance for MPAS-O using RAPL interface**



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