SUNERG

Motivation

Deep Learning & Challenges



Robotics Asimo

Facial Recognition Deep Dense Face Detector Yahoo Labs)

Offline & Online Data Analytics **Real Time News Feed** Facebook)

In the past decade ...

- 10 20x improvement in *processor* speed
- 10 20x improvement in *network* speed
- Only 1.5x improvement in **//O** performance
- I/O will eventually become a bottleneck for most computations

Deep Learning Scaling







- Caffe/LMDB is 660x worse than ideal for 9216 processes
- Read time takes up 90% of the total training time for 9216 processes
- I/O bottleneck is caused by five major problems
 - 1. Interprocess contention -- results in excessive number of context switches
- 2. Implicit I/O inefficiency -- OS fully controls I/O
- 3. Sequential data access restriction -- arbitrary database access is not allowed in LMDB
- 4. Inefficient I/O block size -- I/O request size is too small to be efficient
- 5. I/O randomization -- abundant readers participating in I/O at the same time
- We proposed 6 optimizations that address 5 problems in state of the art I/O subsystem of deep learning

LMDB Inefficiencies

Caffe's I/O Subsystem: LMDB

- Uses Lightning Memory-mapped database (LMDB) for accessing the dataset
- B+-tree representation of the data
- Database is mapped to memory using mmap and accessed through direct buffer arithmetic • Virtual memory allocated for the size of the full file
- Specific physical pages dynamically loaded by the OS on-demand

Pros: makes it easy to manipulate complex data structures (e.g., B+ trees) since LMDB can think of it as fully in-memory

Problem 1: Mmap's Interprocess Contention

Underlying I/O in mmap relies on the CFS scheduler to wake up processes after I/O has been completed

• Processes are put to sleep while waiting for I/O to complete

- I/O completion interrupt is a bottom-half interrupt
- The handler does not have knowledge about the specific process that triggered the I/O operation
- Every process that is waiting for I/O is marked as runnable
- Every reader is woken up each time an I/O interrupt comes in
- This causes a large number of unnecessary context switches



I/O Bottleneck Investigation in Deep Learning Systems

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Sarunya Pumma, Min Si, Wu-chun Feng and Pavan Balaji. *Parallel I/O O* Distributed Systems (ICPADS). Dec. 15-17, 2017, Shenzhen, China.

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Library	Optimization	Reducing Interprocess Contention	Explicit I/O	
LMDB	-			Τ
LMDBIO	LMM	V		
	LMM-DM	v		
	LMM-DIO	v	~	
	LMM-DIO-PROV	v	~	
	LMM-DIO-PROV-COAL	v	~	
	LMM-DIO-PROV-COAL-STAG	v	~	

- virtual address spaces are different

IEEE International Conference on Parallel and

Our Solution: LMDBIO (cont.)

LMDBIO-LMM-DM (cont.)

Part II: Speculative Parallel I/O

- Each process estimates pages that it will need speculatively fetches pages to memory in par
- Then each process sequentially seeks the loca for another processes and sends the cursor t next higher rank process
- The expectation is that the seek can be dor entirely in memory
- Once the sequential seek is done, each reader perform actual data access
- This adds a small amount of extra data reading allows parallel I/O

Estimation of Speculative I/O

- The estimation of number of pages to fetch is on the first record's data size
- I.e., CIFAR10-Large record's size is 3 KB, w ~1 page. To read *n* records, it needs to fetc pages
- The estimation of the read offset is performe same fashion
- Estimation of the "approximate" start and end location for each process is important
- If the estimate is completely wrong, we will reading up to 2x the dataset size (still bett the LMDB)

LMDBIO-LMM-DIO

Optimization: Replace mmap with POSIX I/O

	Timeline	
PO		4
D1	seek (mmap)	read data to shared buffer (PO
PI	wait	read data to shared buffer (PO
PZ	wait	read data to shared buffer (PO
	coattor off	cotc

LMDBIO-LMM-DIO-PROV

Optimization: Utilize provenance information to e replace mmap with POSIX I/O

- Making a case for storing data provenance in deep learning (how the data was created)
- LMDB's database layout can be deterministic information of how it is created is provided
- We can compute exactly where the data page
- Sequential seek can be completely eliminated
- All I/O operations can be done via direct I/O (mcompletely removed)

LMDBIO-LMM-DIO-PROV-COAL

Optimization: Coalesce multiple batches of data to at once to allow direct I/O to benefit from large I/C

- We read a larger chunk of data to enlarge I/O ti eliminate the skew in I/O
- A constant amount of memory is kept aside reading
- We read multiple batches of data at once

Results

Time (s)	10000 - 1000 - 100 -			LMDB LMDBIO- LMDBIO- LMDBIO- LMDBIO- LMDBIO- LMDBIO-	LMM LMM-DIO LMM-DM LMM-DIO- LMM-DIO-	PROV PROV-CO, PROV-CO,
Pl: • •	atform: Au InfiniBand 110 TB G Each nod • 2 Sand Pentiuu hypert • 64 GB	rgonne [*] d Qlogic PFS sto e y Bridg m Xeon hreadir memor	2 s Blues QDR orage e 2.6 GH (16 core g disable	4 Data Netv MPI: z es, ed)	8 Iset: CIF Work: Al MVAPIC	¹⁶ AR10-L exNet H-2.2

• 15 GB RAM disk



		Database P	Part II: Parallel I/O an	nd in-memory sequ	ential seek	
d and <mark>allel</mark>		D0	D1	D2	D3	
ation o the	I rrent	P0 reads	→	→		
	Conct		P1 reads 🛛 🛶	P2 reads		
le	L L	4		54	P3 reads	
r can .ख	ry)	PU seeks	PO sends cursor t	o P1 P1 sends cursor t	o P2	
g, but	-memo		P1 seeks P1 accesses	P2 seeks	P2 sends cursor to P3	
х Х	(in			P2 accesses	P3 seeks ···	
based	l				P3 accesses	
hich is E	stim	ation of Specu	lative I/O (com	t.)		
 We use a history-based training for our estimation We correct our estimate in each iteration depending on the actual data read in all of the previous iterations The general ideal of out correction is that we attempt to expand the speculative boundaries to reduce the number of missed pages Initial iterations might be slightly inaccurate, but we converge fairly quickly (1-2 iterations) 						
SIX I/O) ···· SIX I/O) ···· SIX I/O) ····	•	 record The root prosecting the consecting the consecting the consecting the consecting the consecting the consecting of the consection of	cess gets offse database using eek is unavoida c rocesses recei eading using PO data using MPI	ets of all data s mmap ble because th ve their offset OSIX I/O shared buffer	amples by ne offsets are not s from root and as same as LMM	
entirely		Important Not	es			
ormation fo	or	Provenance I MDB form	e information <mark>is</mark> at	<mark>not</mark> stored in	the original	
only if the s are locate map is	ed	 This is an We use a se This file of generate database It is much hundred 	extension that parate auxiliar can be created ed or later using e n smaller than t bytes)	t we are propo y file to store t while the data g a one-time re he dataset itse	sing this information base is being ad of the elf (a few	
		LMDBIO-L	MM-DIO-	PROV-CO	AL-STAG	
o be read) size ime to for data		 Optimization randomizatio I/O stagge Readers number Only one MPI_Send a 	Adopt I/O stag n ring technique are divided into of members group can per and MPI_Recv a	ggering to redu orders the req o multiple grou form data read ire used in the	uce I/O uests ips with the same ling at a time implementation	

